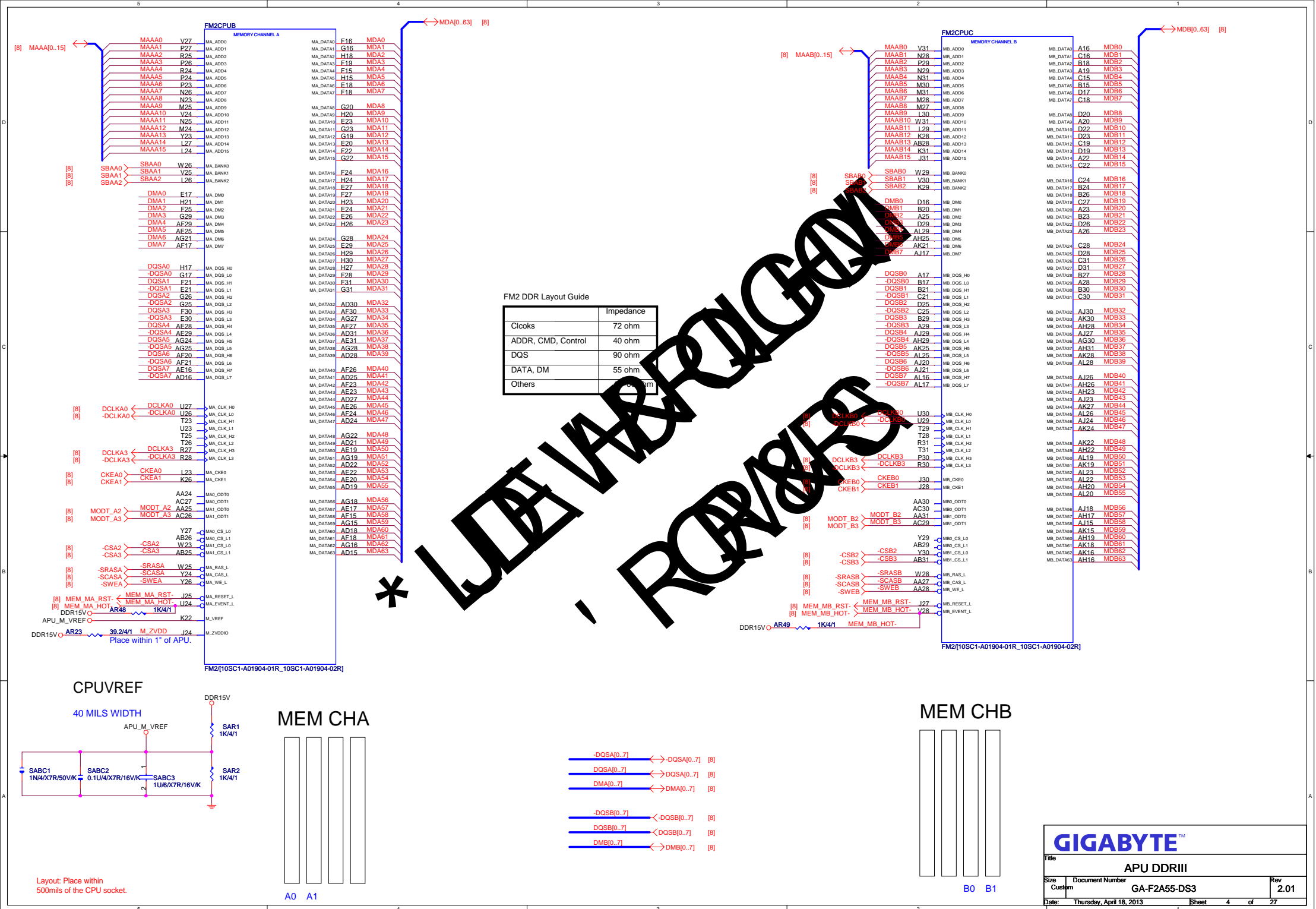
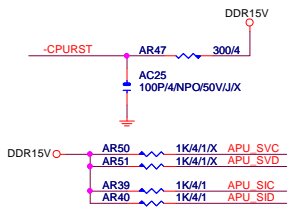


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P-Code : U12031-0
Tip/Top : 9MF255DS3-00

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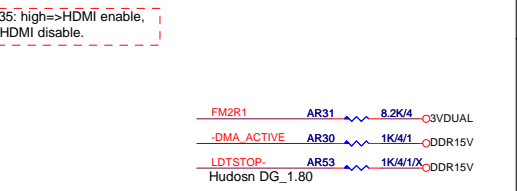
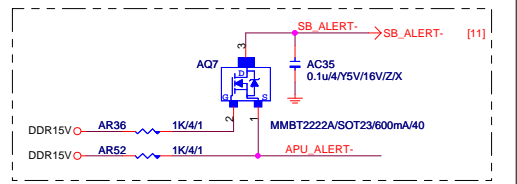
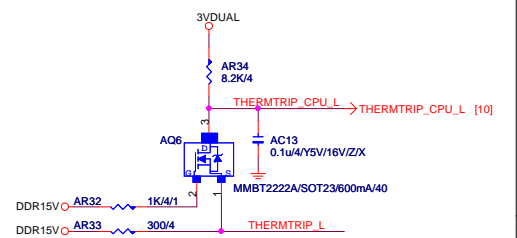
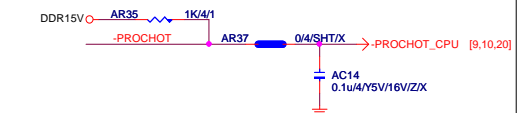
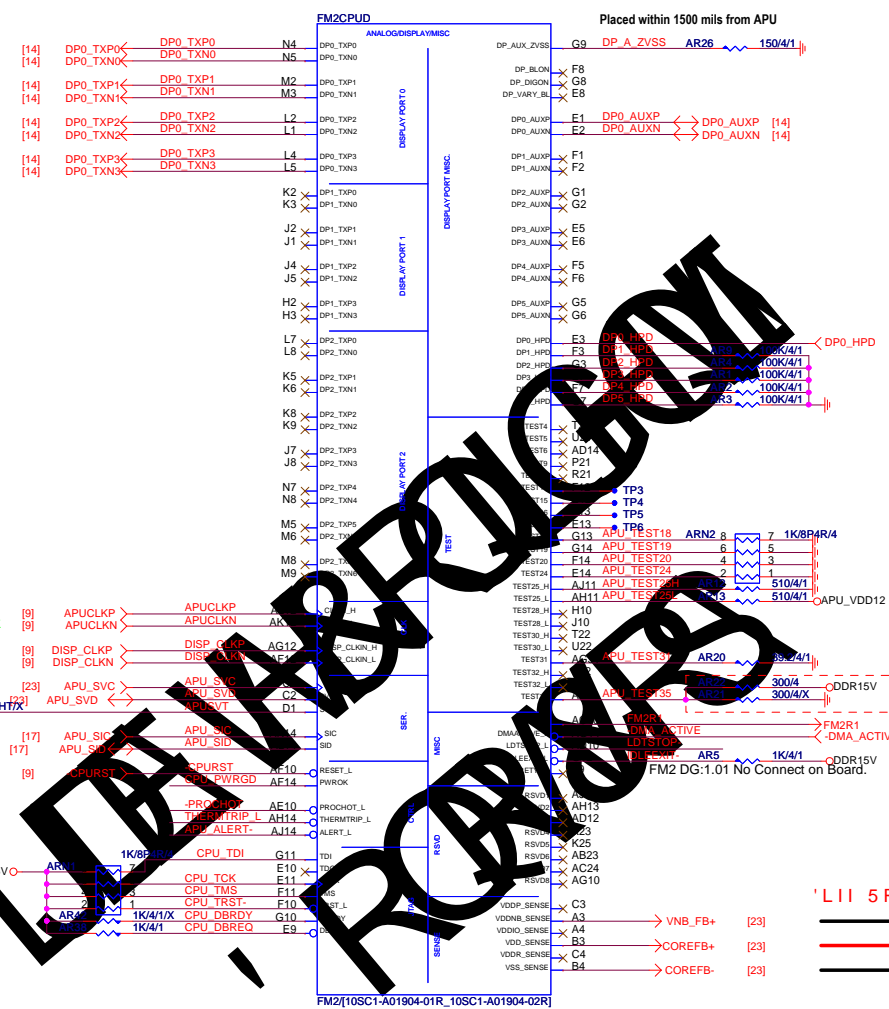
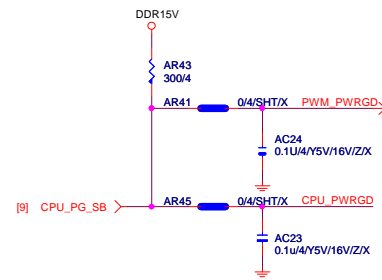




SVI 2.0 max frequency=20MHz.
Resolution=6.5mV

SVT=Serial VID Telemetry for
APU get PWM information used.

APU Spread 100Mhz clock
DP Non-Spread 100Mhz clock



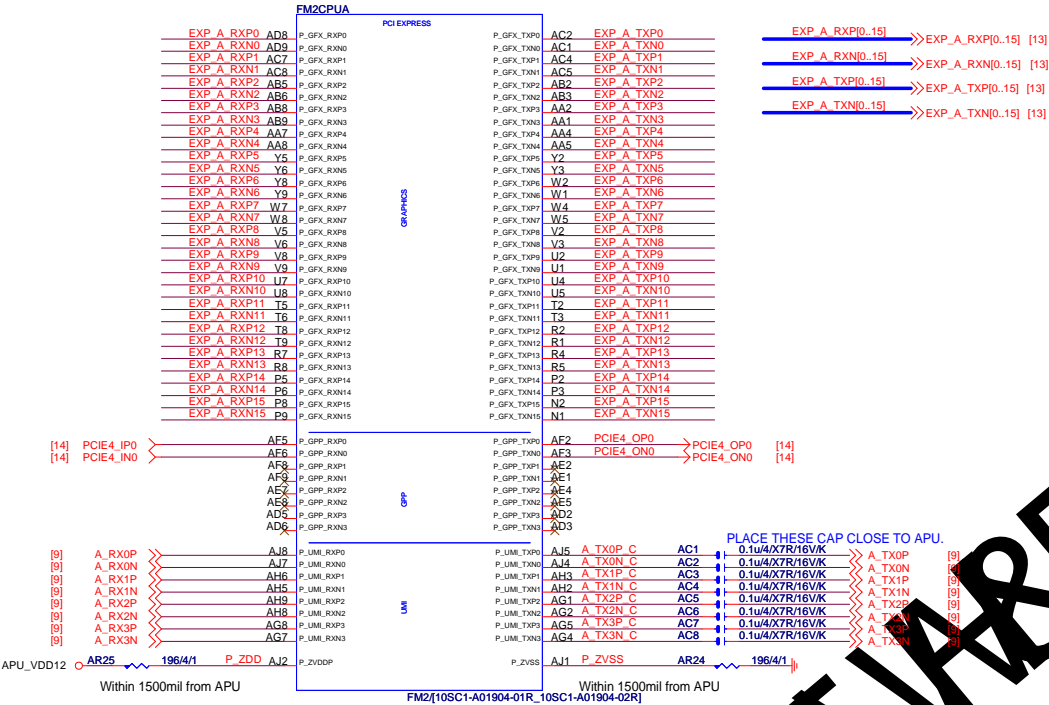
'LII 5RXQW \$38 WR 3:0

9 1 % B)
& 2 5 () %
& 2 5 () %

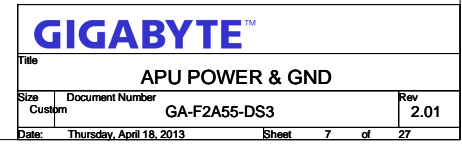
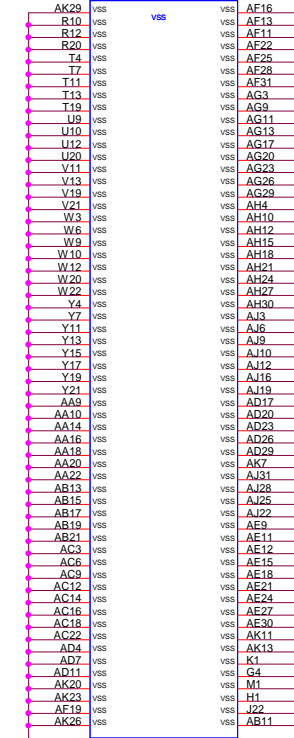
Group	APU FM2
Group A	VDDA25:1.8~2.7V/0.5A
Group A	DDR15V:0.8~2.3V/30A
Group B	VCORE: 0.8~2.0V/120A
Group B	VCORE_NB:1.2V/50A
Group B	VDDP:1.2V/5A
Group B	VDDR:1.2V/5A

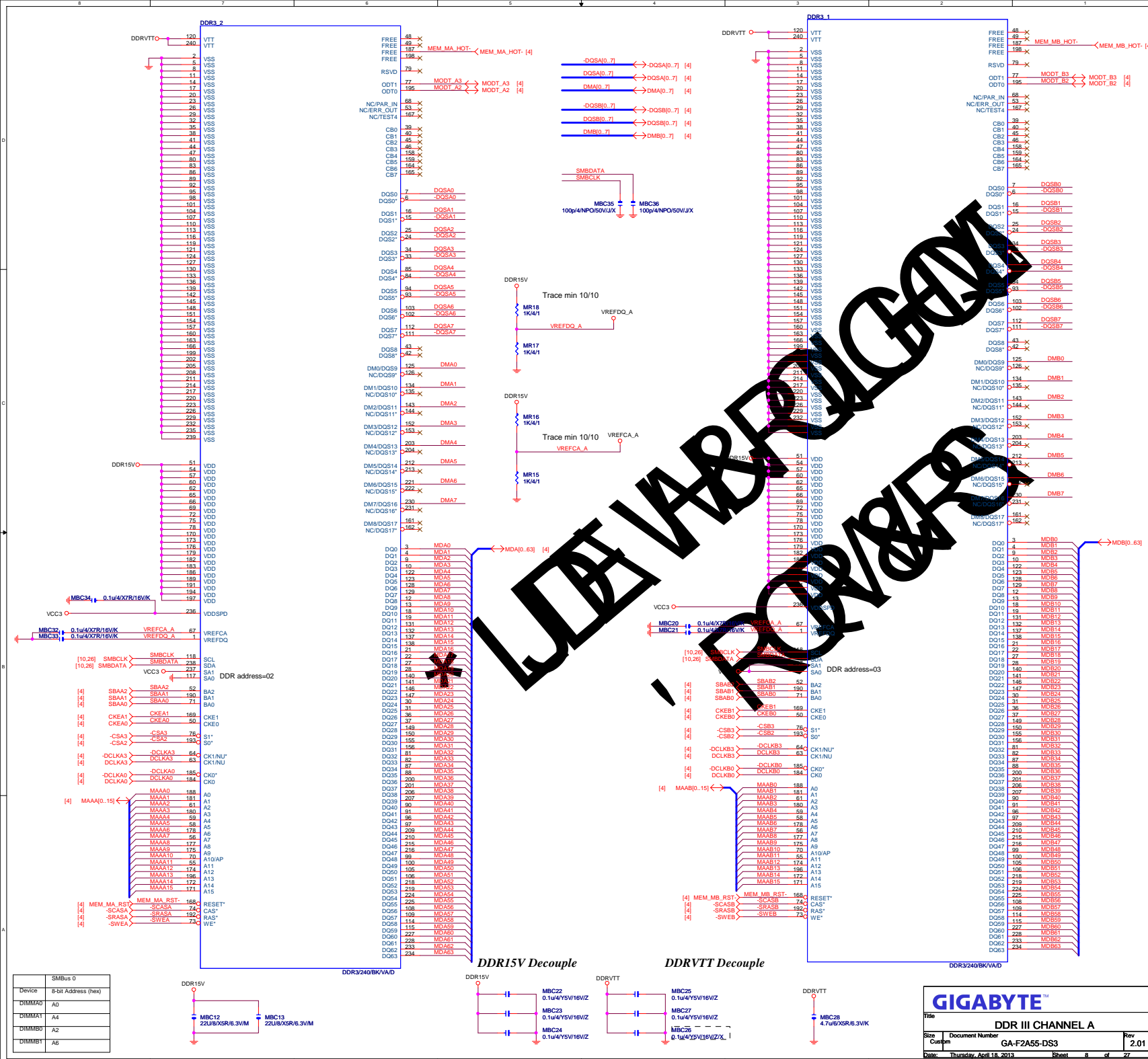
Group A must early than Group B

GIGABYTE™		
Title	APU CONTROL	
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Date:	Thursday, April 18, 2013	Sheet 5 of 27



* UNRELIABLE

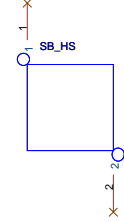






PLACE THESE PCIE AC COUPLING CAPS CLOSE TO SB850

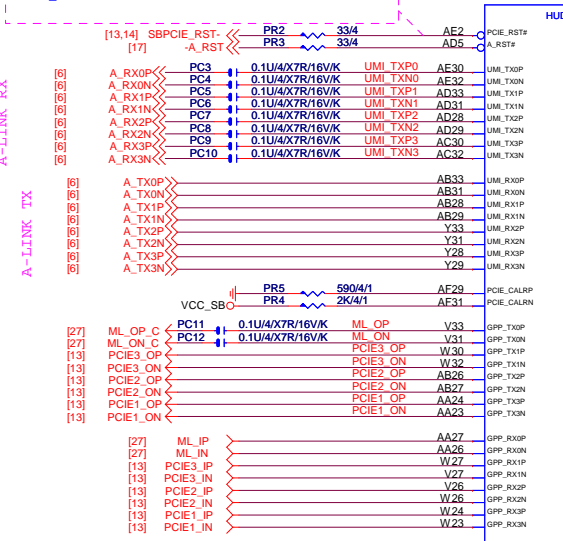
S.B HEATSINK



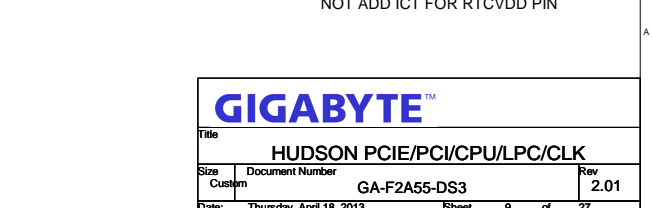
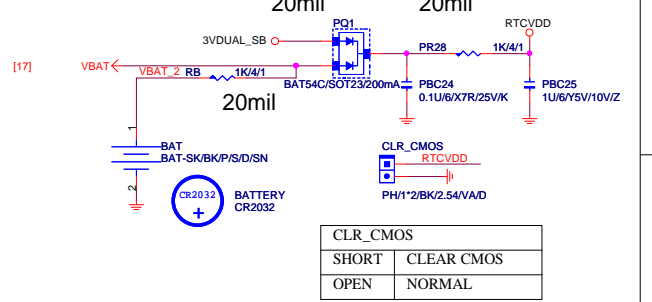
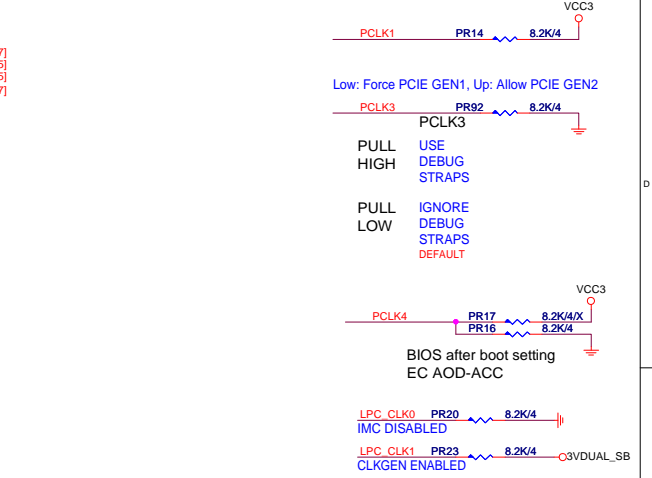
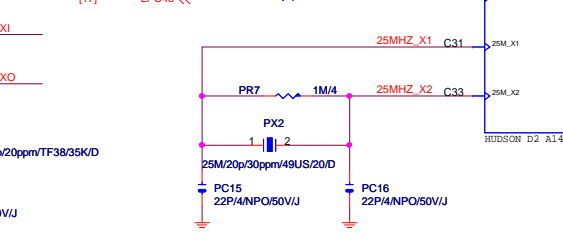
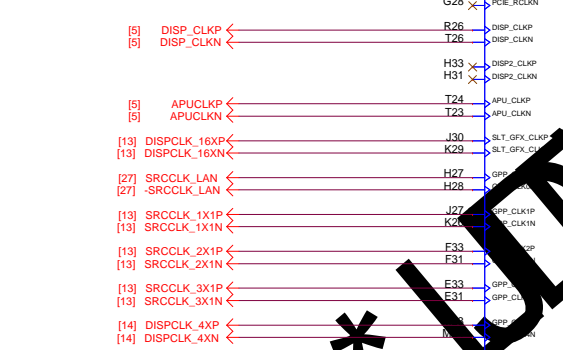
SB_HS(12SP2-SA0503-11R_12SP2-SA0503-12R_12SP2-SA0503-13R)

SHW/D0.64*5.08*6.74

PCIE_RST# connect to APU PCIe Devices/Slots



VCC_SB 2K/4/1

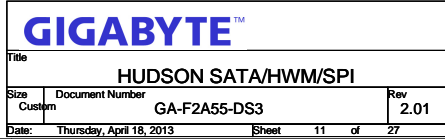


GIGABYTE

HUDSON PCIE/PCI/CPU/LPC/CLK

Size	Document Number	Rev
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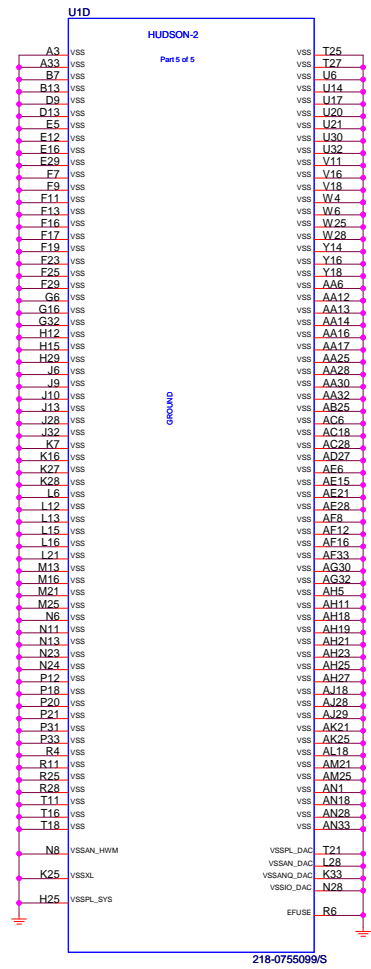
PLACE ALL THE DECOUPLING CAPS ON THIS SHEET CLOSE TO SB AS POSSIBLE.

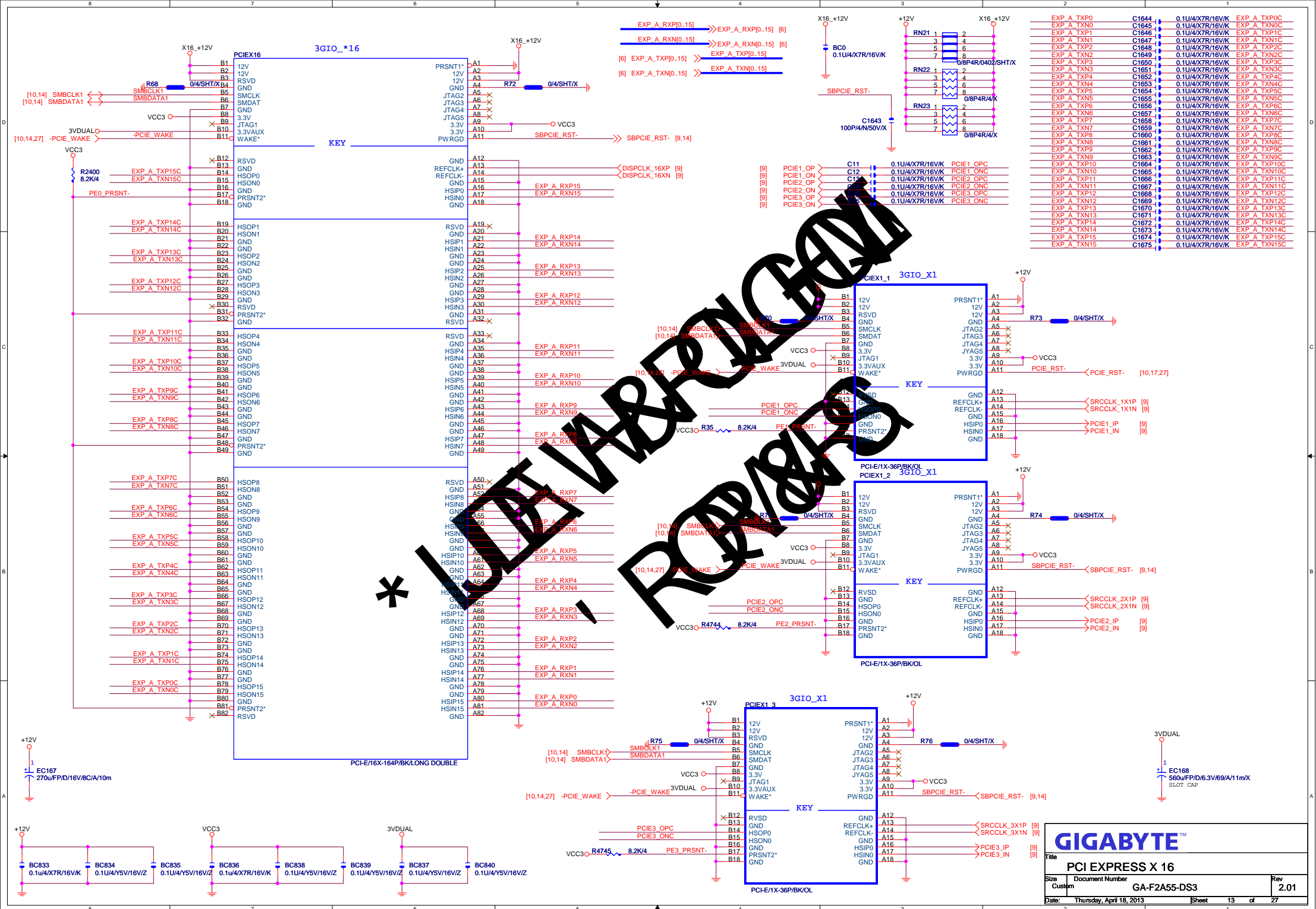
VGA Translator Not Implemented.

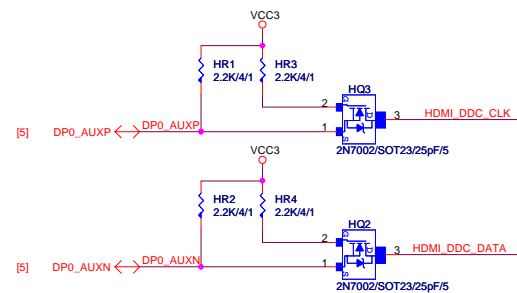
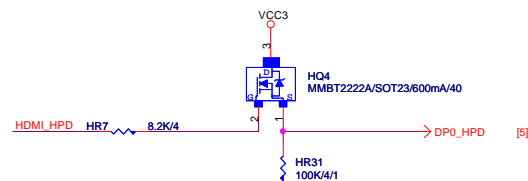
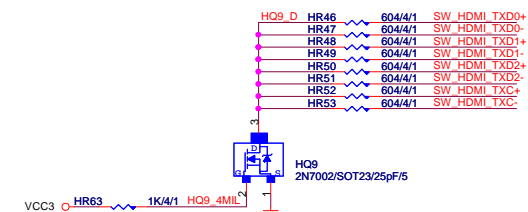
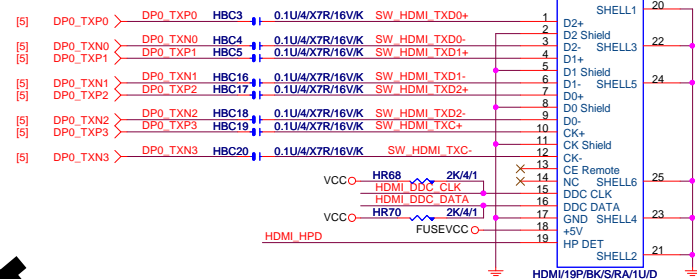
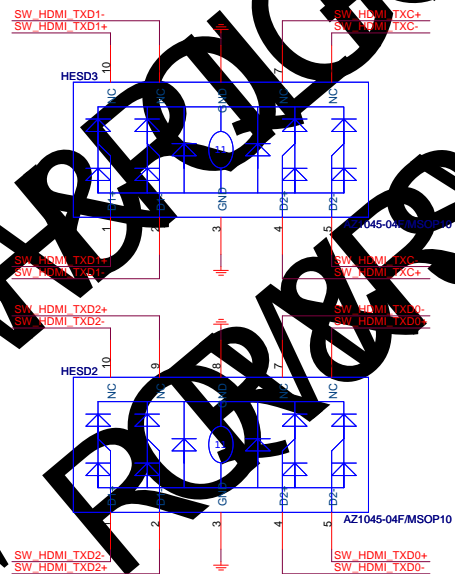
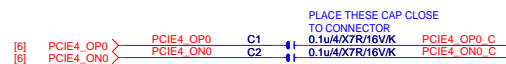
VGA Translator Not Implemented.

Hudson 3/4 does not support an RGMII/MII interface.

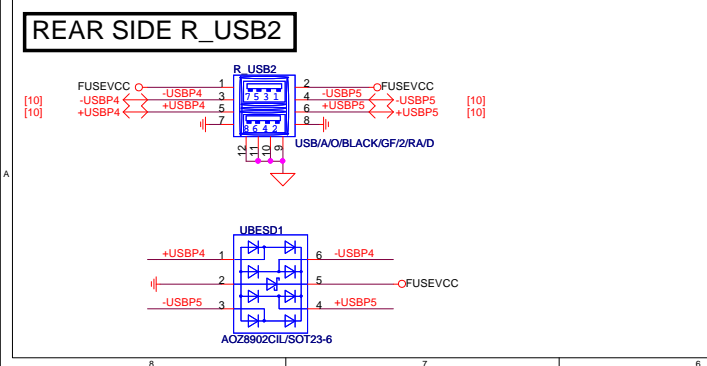
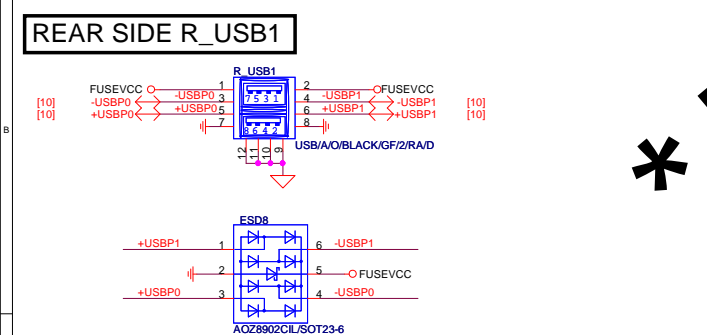
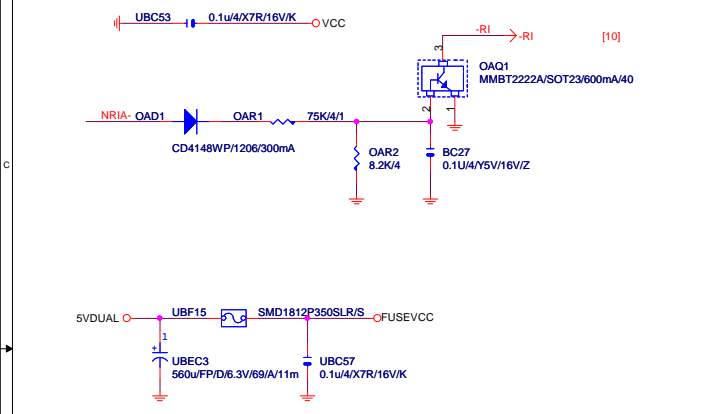
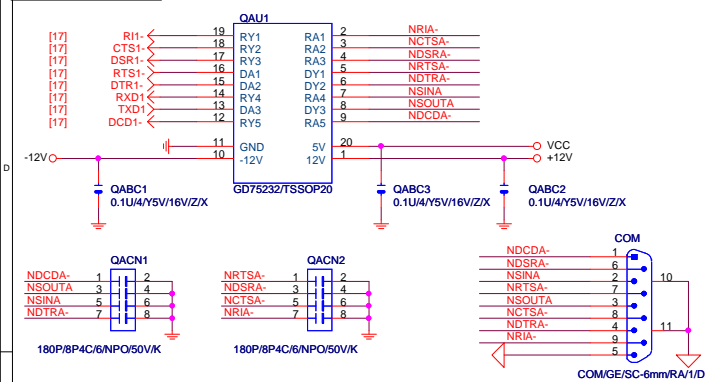
* 218-0755099/S



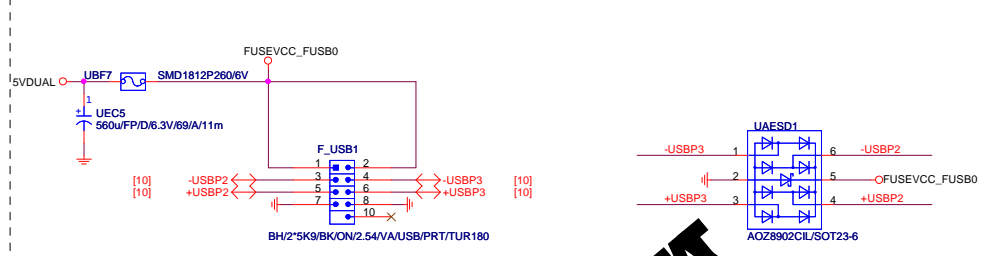




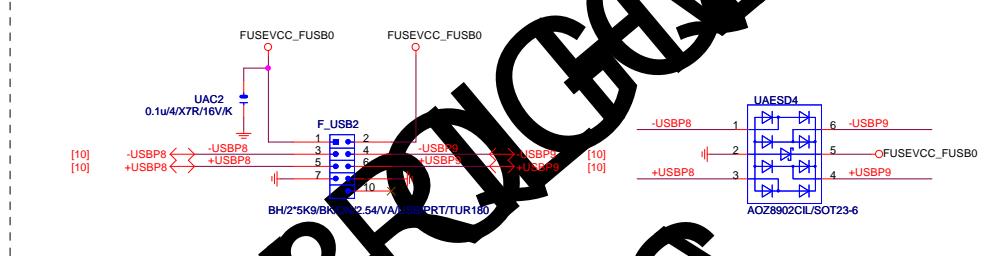
COM POR



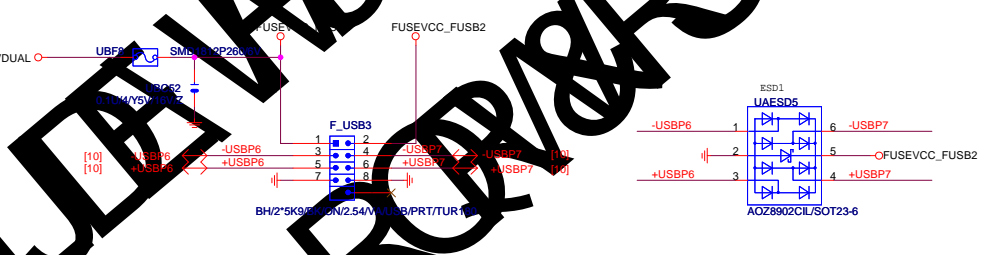
FRONT SIDE USB1



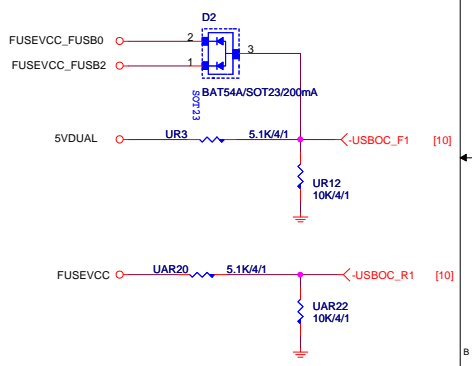
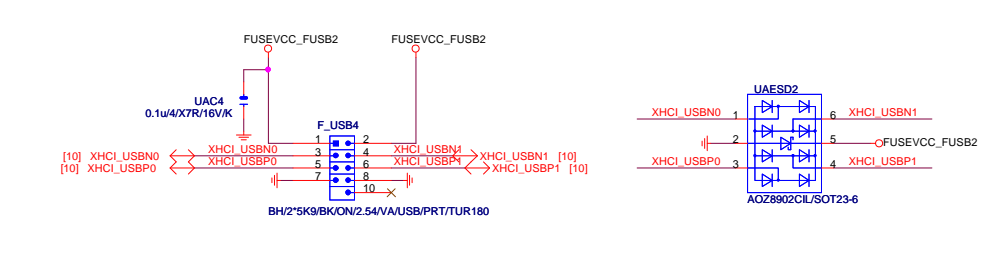
FRONT SIDE USB2

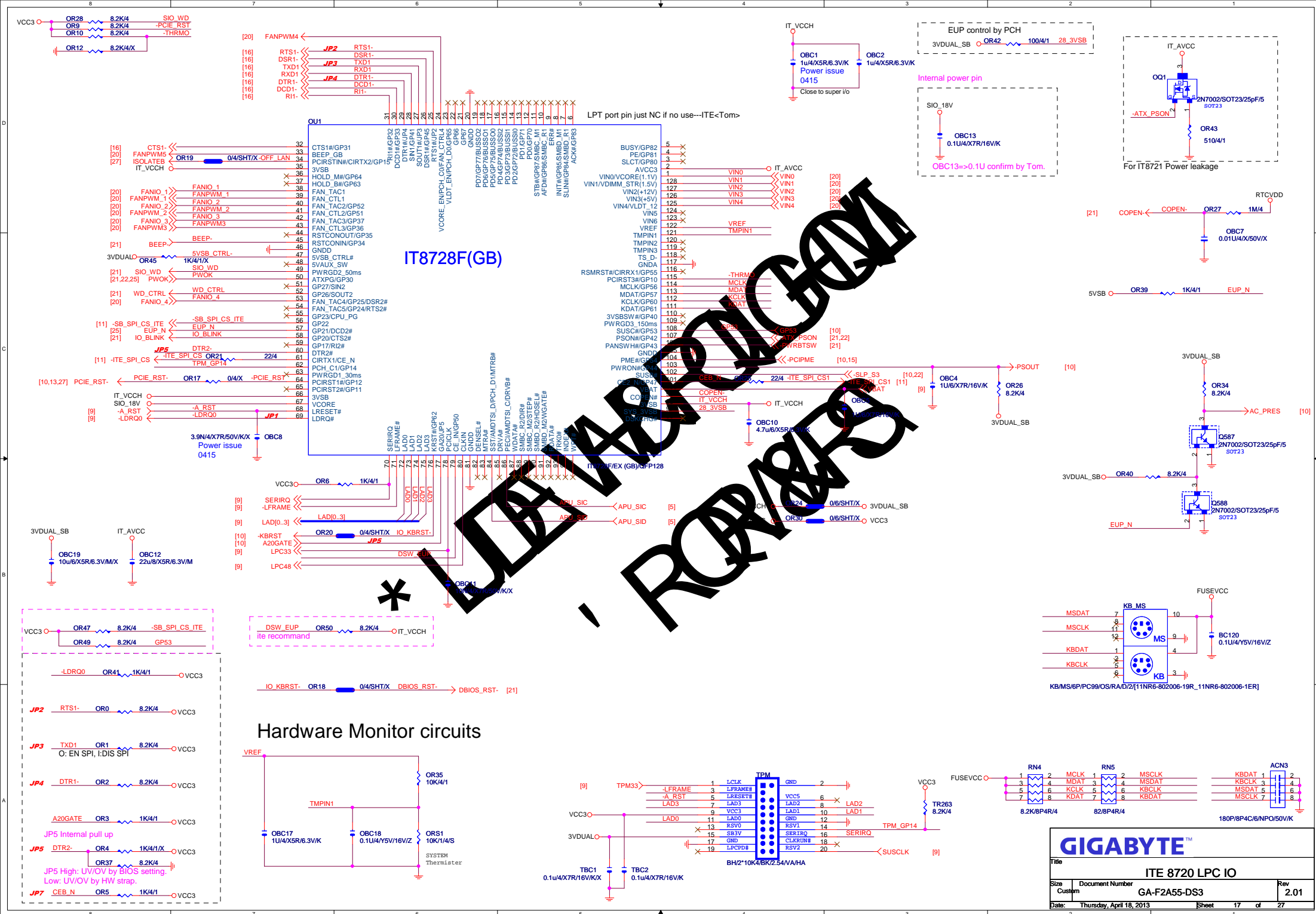


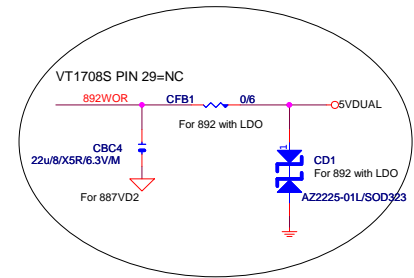
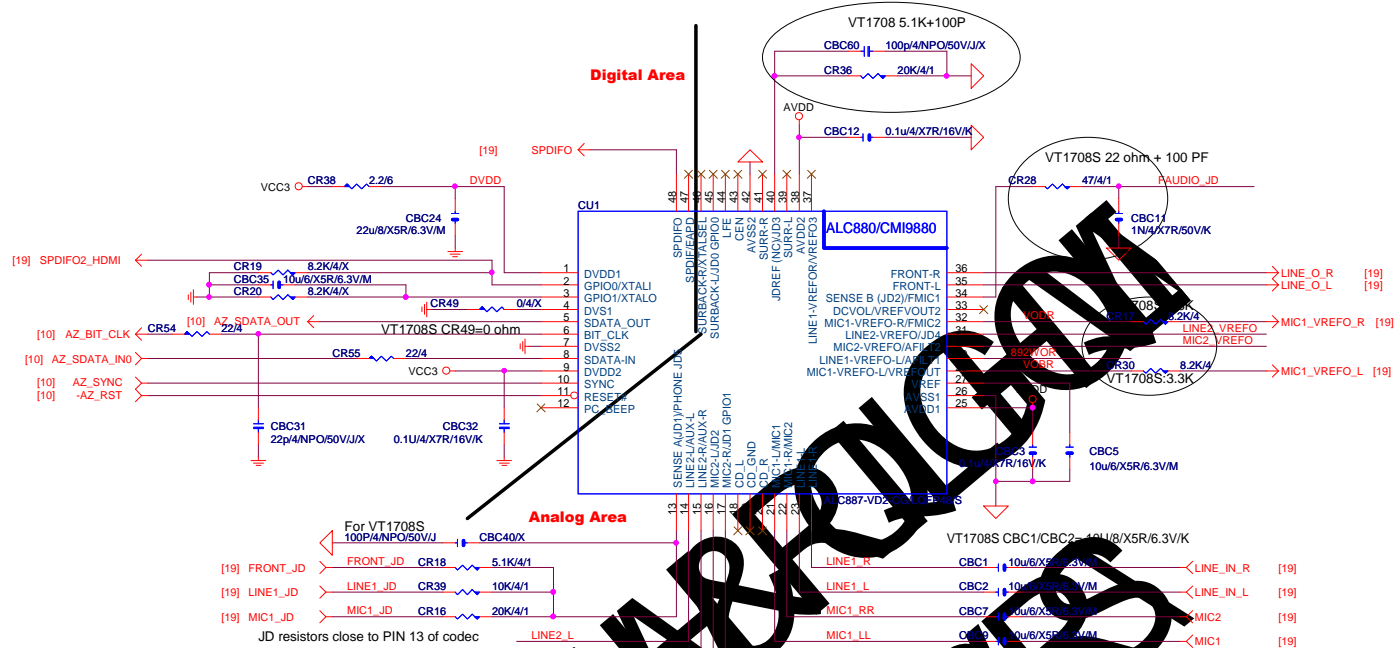
FRONT SIDE USB3



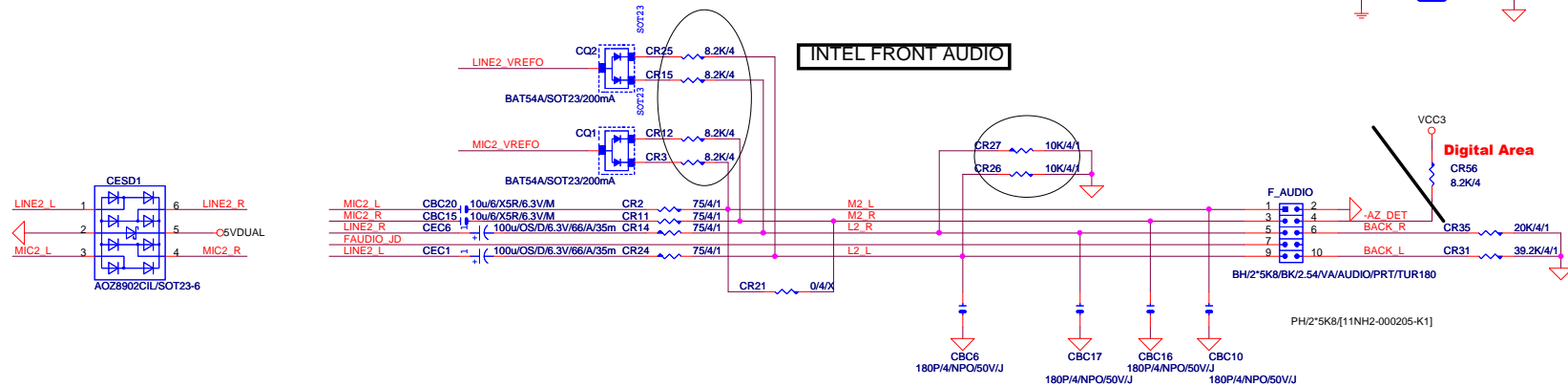
FRONT SIDE USB4

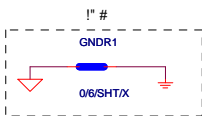




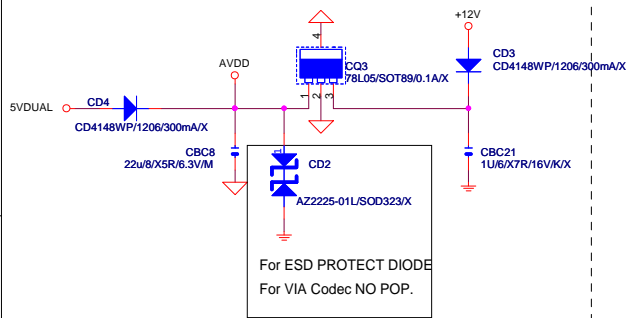
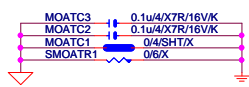
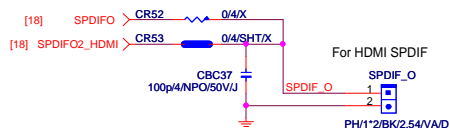


For VT1708S:3.3K

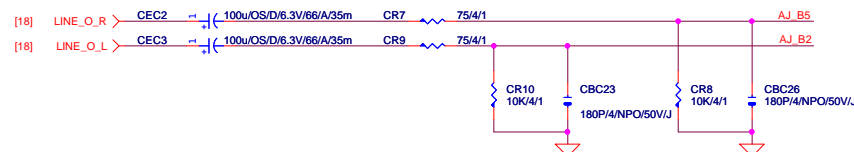




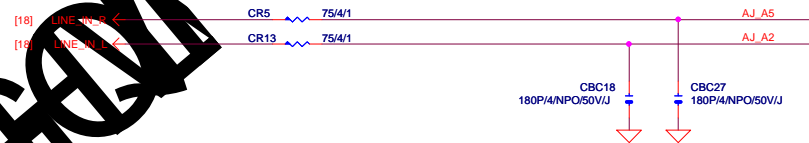
SPDIF



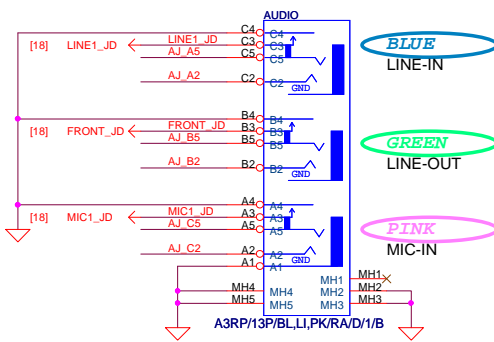
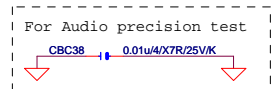
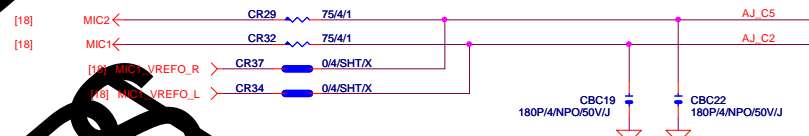
LINE OUT
FRONT OUT



LINE-IN



MIC



A3RJ/13P/B/[11NR6-403006-01_11NR6-403006-02]
3RJ+15P/[11NR6-403004-11]

GIGABYTE

Title

AUDIO JACK

Size Custom

Document Number

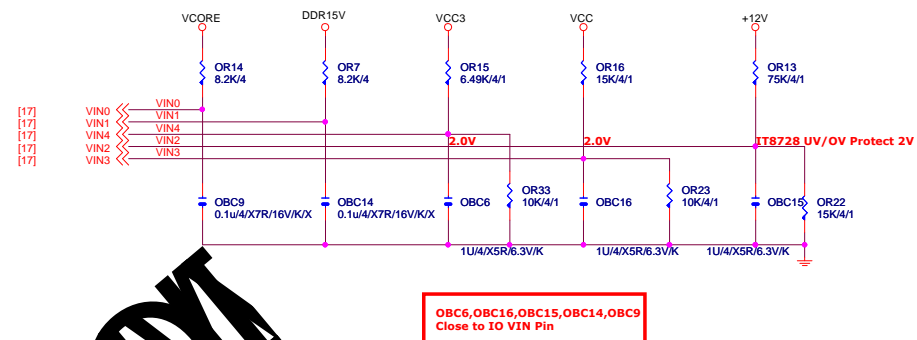
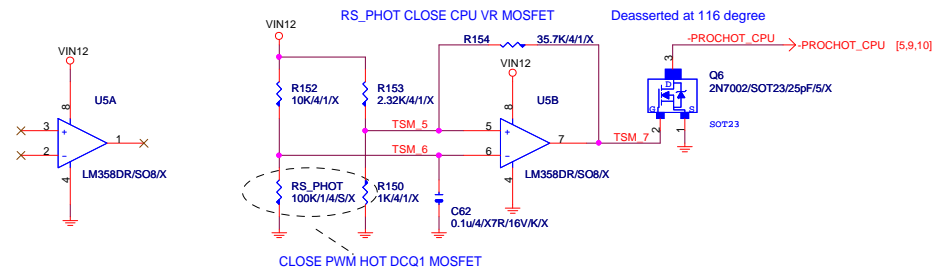
GA-F2A55-DS3

Date: Thursday, April 18, 2013

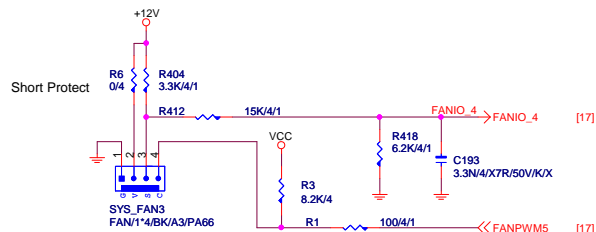
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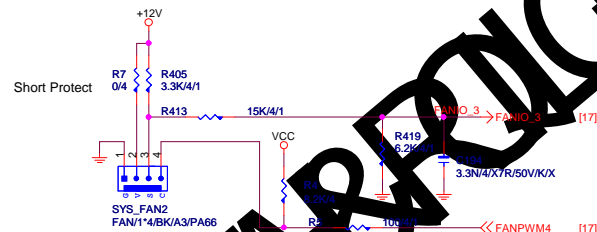
MOSFET HOT



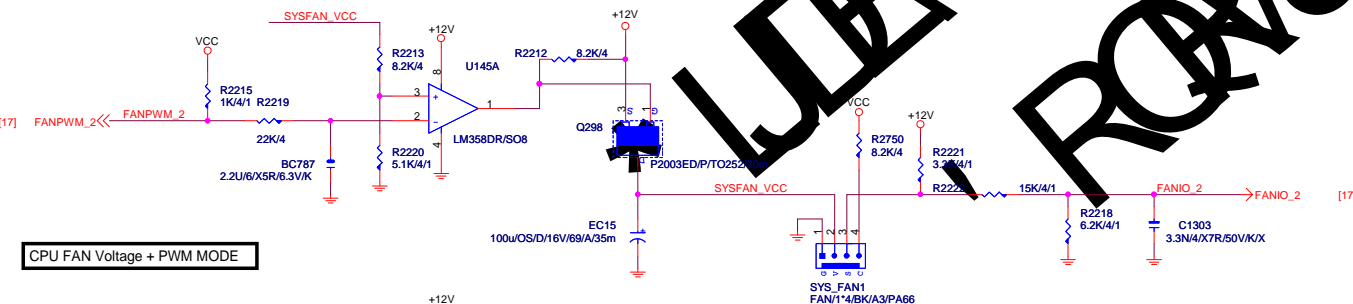
SYSTEM FAN3 PWM MODE



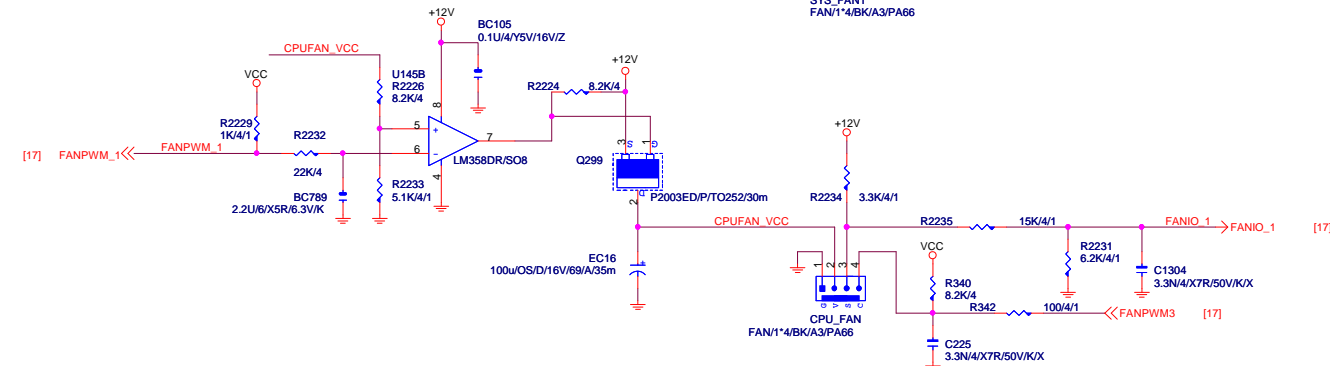
SYSTEM FAN3 PWM MODE



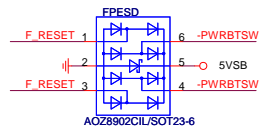
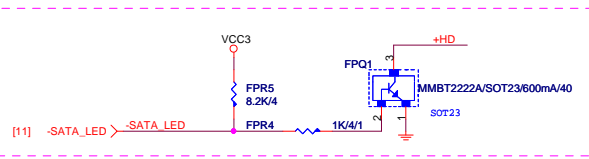
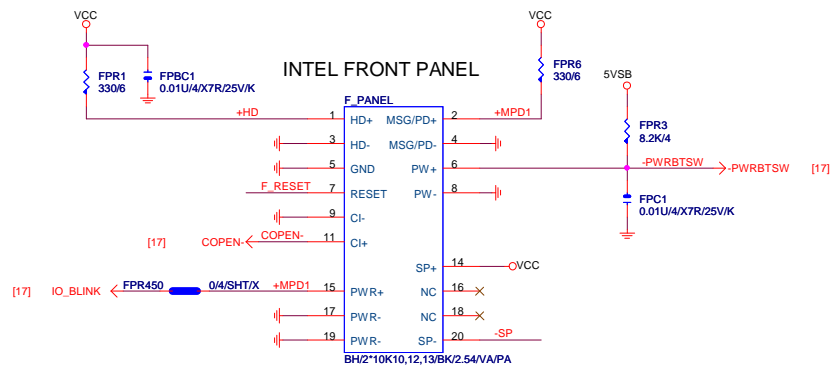
SYSTEM FAN1 Voltage MODE



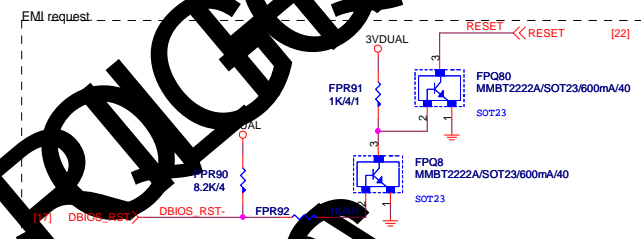
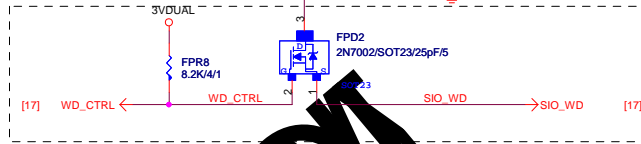
CPU FAN Voltage + PWM MODE



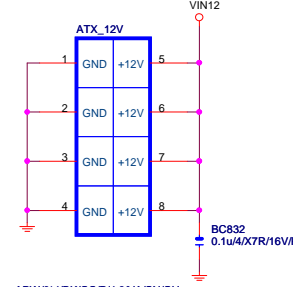
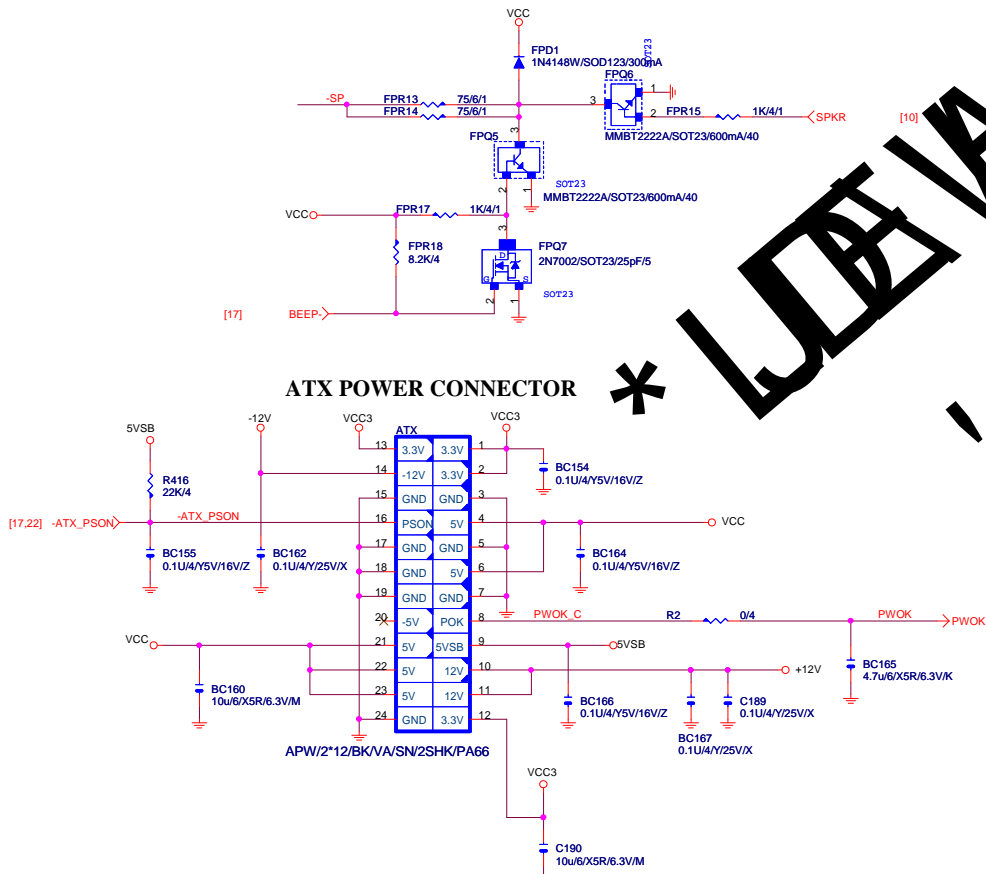
INTEL FRONT PANEL



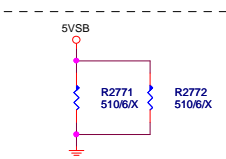
Watch Dog Patch S3 Fail,



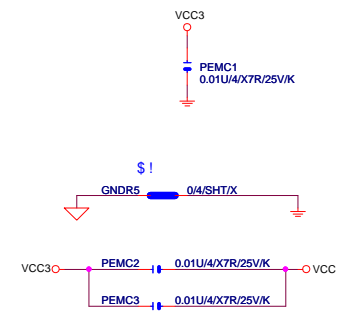
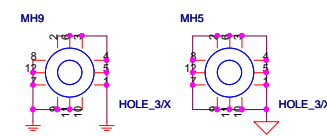
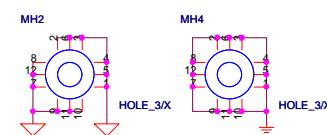
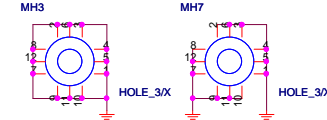
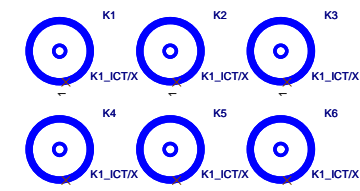
ATX POWER CONNECTOR



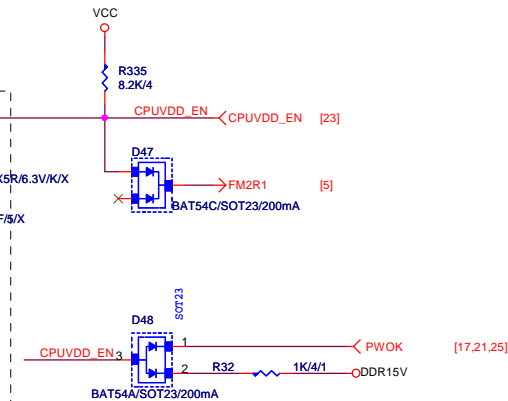
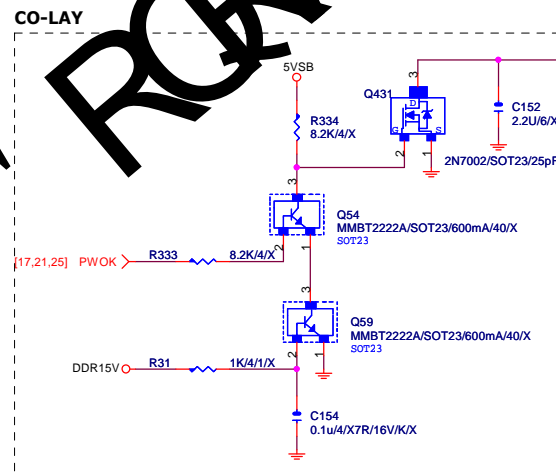
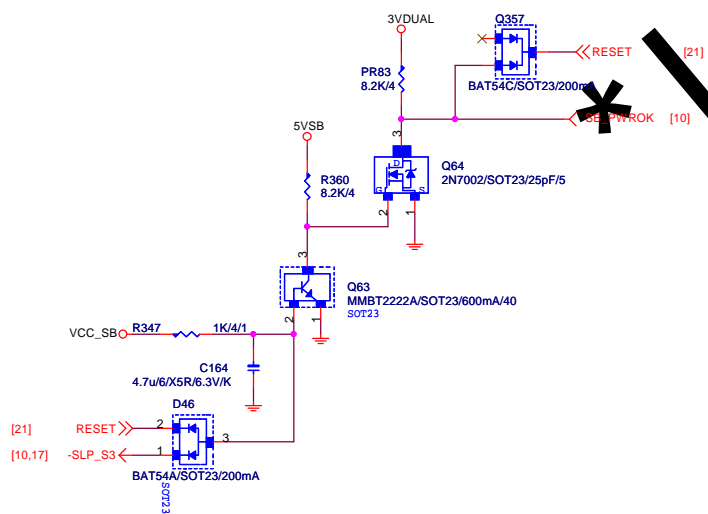
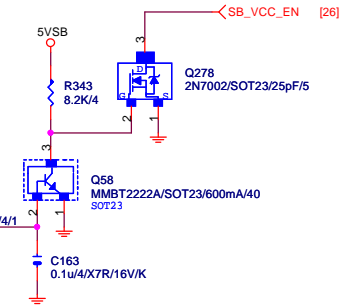
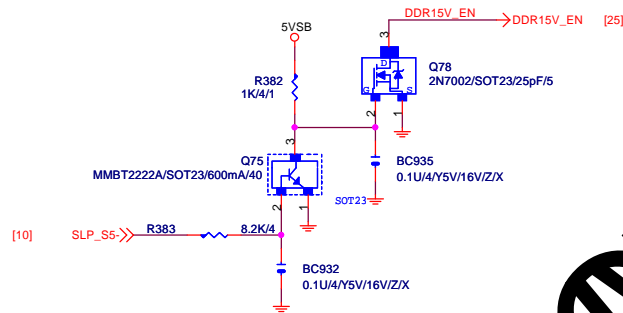
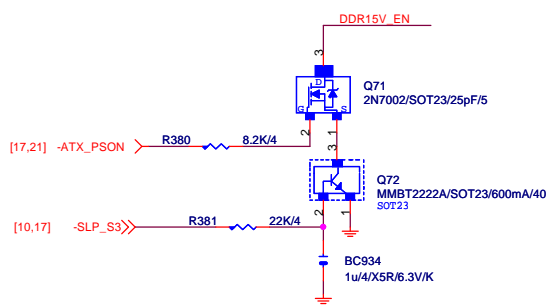
APW/2*4/BK/OC/P/4.2VA/SN/OH

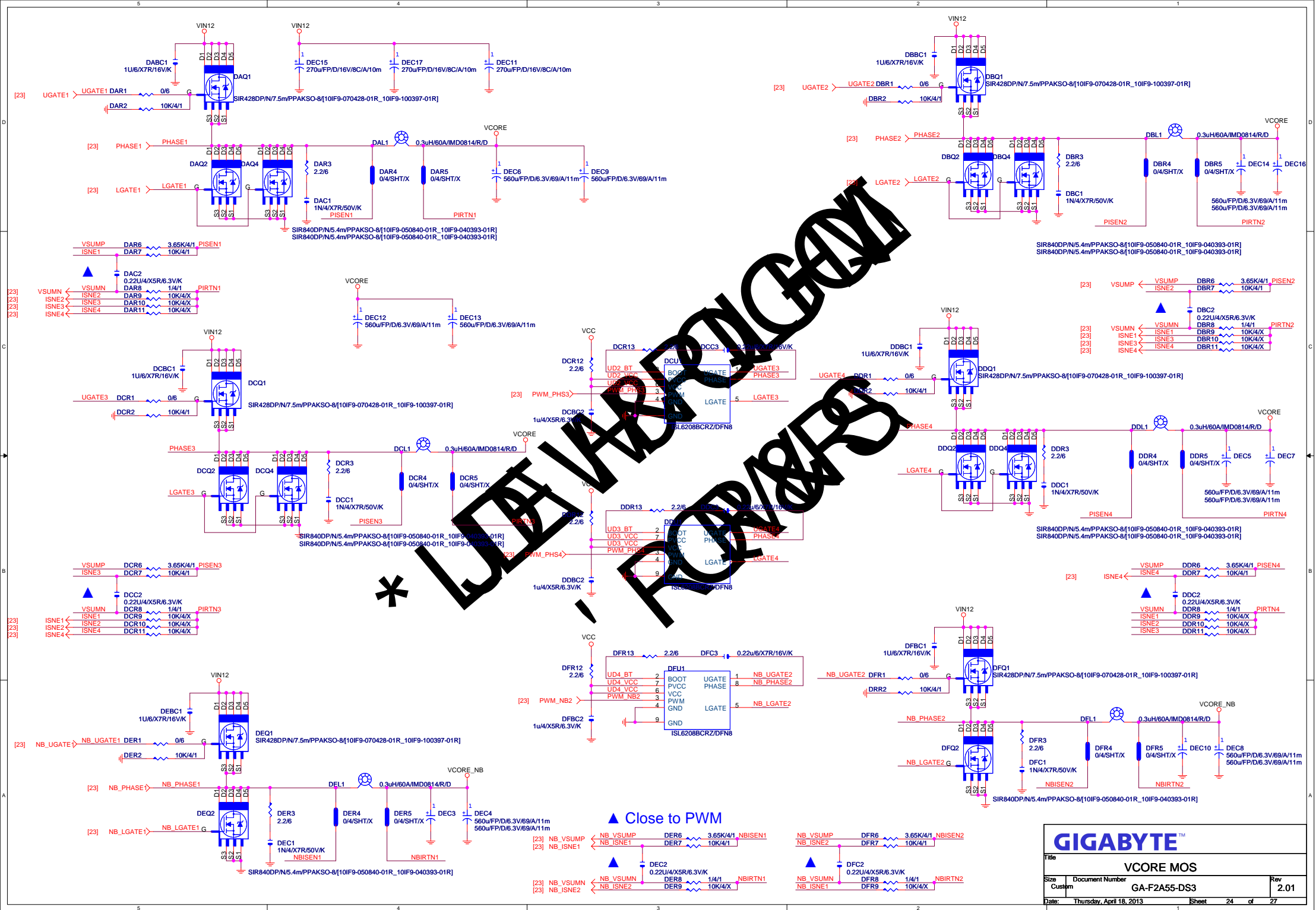


For Seasonic 900W
Power supply
cant Boot issue



GIGABYTE™			
Title			
ATX, FRONT PANEL			
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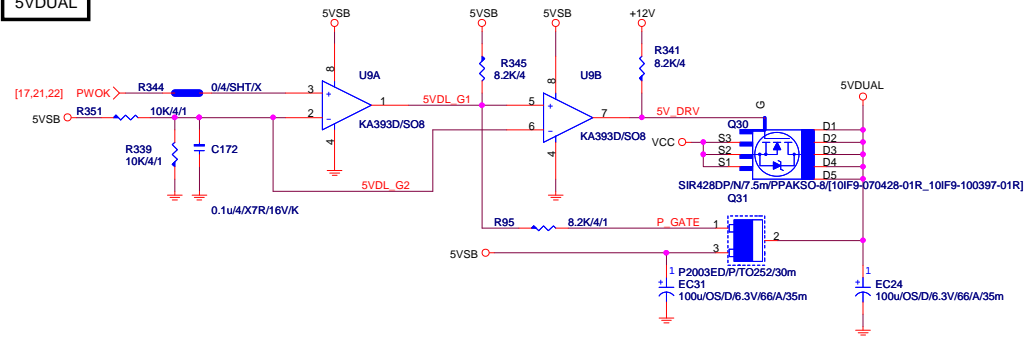




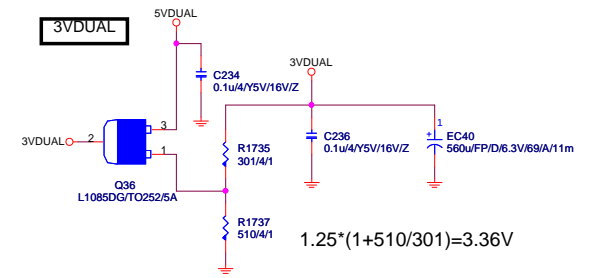
GIGABYTE

Title			VCore MOS
Size	Document Number	Rev	
Custom	GA-F2A55-DS3	2.01	
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5VDUAL

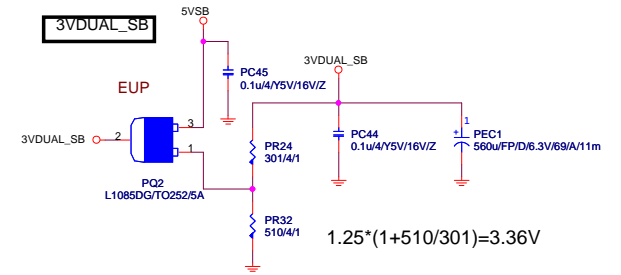


3VDUAL



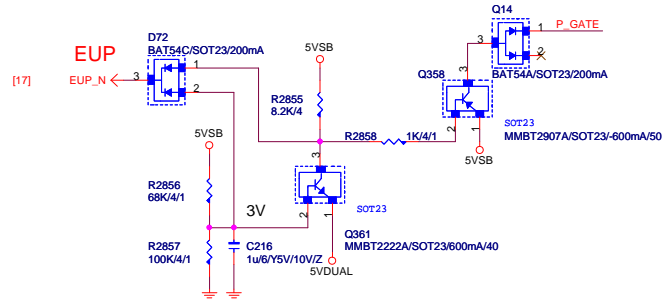
$$1.25 \times (1 + 510/301) = 3.36V$$

3VDUAL_SB

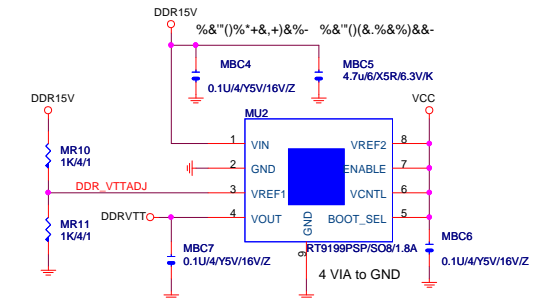
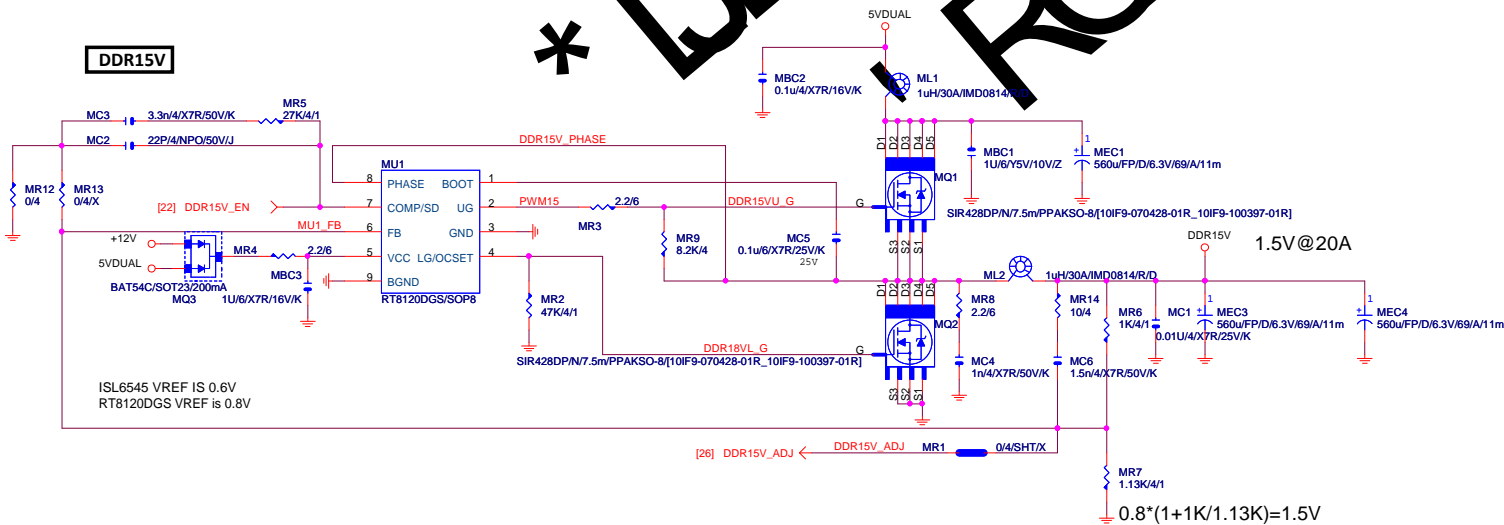


$$1.25 \times (1 + 510/301) = 3.36V$$

EUP



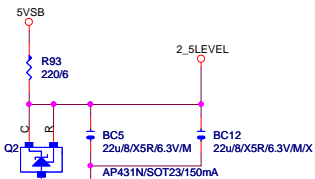
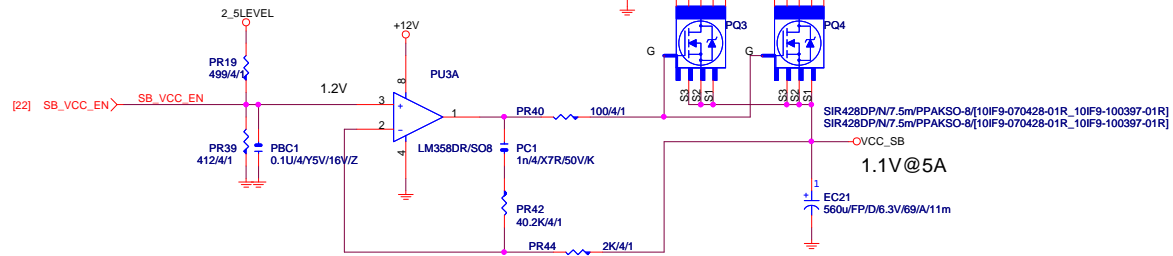
DDR15V



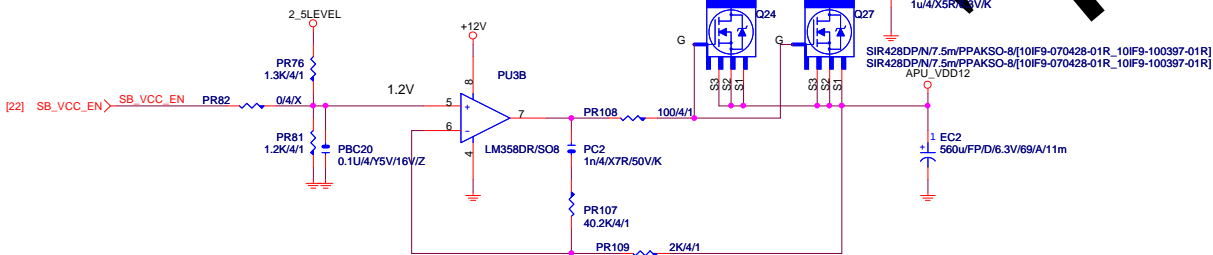
GIGABYTE

Title		
DDR POWER , VCC18		
Size	Document Number	Rev
Custom	GA-F2A55-DS3	2.01
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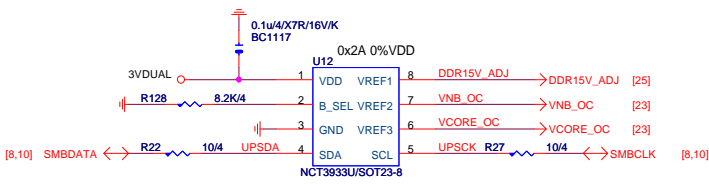
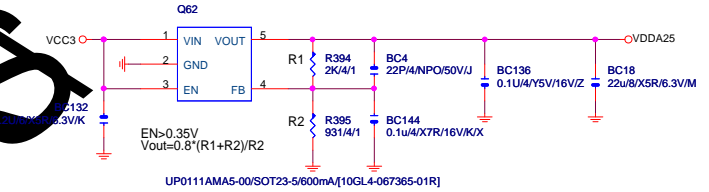
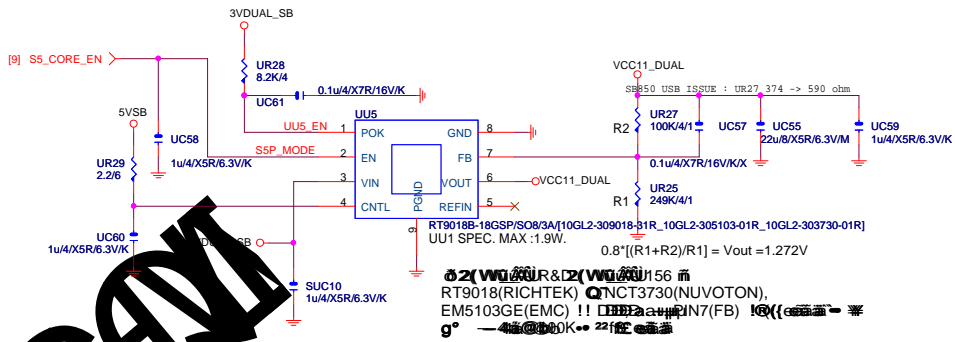
VCC_SB



APU_VDDP



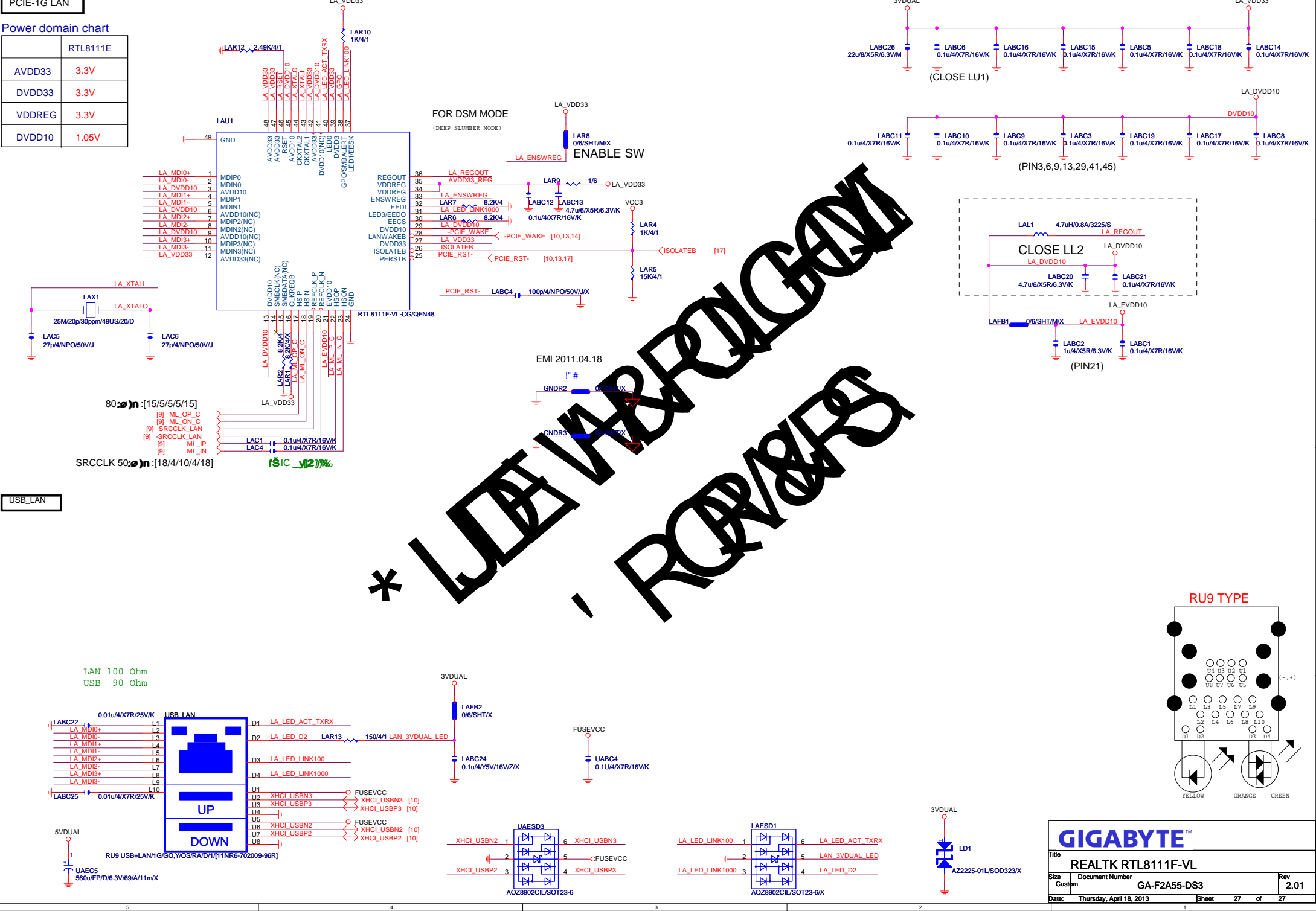
VCC11_DUAL



PCIE-1G LAN

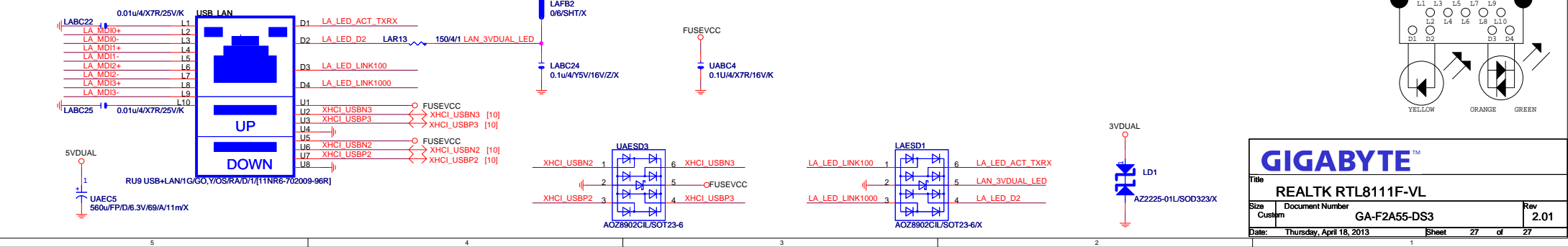
Power domain chart

	RTL8111E
AVDD33	3.3V
DVDD33	3.3V
VDDREG	3.3V
DVDD10	1.05V



USB LAN

LAN 100 Ohm
USB 90 Ohm



REALTK RTL8111F-VL

Size: Custom

Document Number: GA-F2A55-DS3

Date: Thursday, April 18, 2013

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